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## **Publications**

RESILIENT MICRORING RESONATOR BASED PHOTONIC NETWORKS, International Symposium on Microarchitecture Proceedings of the Fifth IEEE/ACM International

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VLSI Design, International Conference on. Persistent Link: VLSI Design, 2004. Proceedings. 17th International Conference on VLSI Design, 2003. Proceedings

### **November 1988 - springer**

November 1988 November 14-17 December 15 18 Bangalore, India Second International Workshop of VLSI Design (Computer Society of India)

### **Richard b brown - publications - faculty profile -**

Publications Publications for Biomedical and Chemical Research," in Proc. International SoC Design on Very Large Scale Integration Systems Manuscript

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### **Vlsi design : proceedings / the international**

VLSI Design 2009: Improving Productivity through Higher Abstraction, The 22nd International Conference on VLSI Design, New Delhi, India, 5-9 January 2009.

### **Vlsi design : proceedings of the second**

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### **Detailansicht - tugrazonline - technische**

IEEE transactions on very large scale integration (VLSI) Second International Workshop, LightSec 2013, Mumbai, India, December 2013, Proceedings

### **Publications of chittaranjan mandal - ernet**

Bangalore, India, pp 1-6, December 9-11, 2009 , Proceedings of Second International Conference on , Proceedings of VLSI Design '90, Bangalore, pp. 18

### **Spie | proceeding | vlsi design of inverse-free**

VLSI design of inverse-free Berlekamp-Massey algorithm for Reed-Solomon code

### **Proceedings of technical program of 2014**

IEEE Catalog Number: ISBN: CFP14847-POD 978-1-4799-2777-7 Proceedings of Technical Program of 2014 International Symposium on VLSI Design, Automation and Test

**Department of computer science and engineering**

Second international Workshop on Privacy Data Management Proc. of 5th Intl. Conf. on VLSI Design, Bangalore, India IEEE Computer Society Press. December, 1988.

**Staff detail ::: klncce**

Proceedings of the Second International Conference on Soft Computing for Problem December 2012. (Springer India) Workshop on VLSI Design Laboratory

**Iwvdt - proceedings of ieee international**

Proceedings of IEEE International Workshop on VLSI Design and Video Technology. Publications: 126 | Citations: 156. Fields of study: Engineering

**Curriculum vitae - indian statistical institute**

00, '01, 04), ADCOMP ('97, '98), VLSI Design, ('94, '95 December 18-21, Bangalore, India, Proceedings of Second International Conference and

**Modeling concepts for vlsi cad objects**

we identify modeling concepts that are fundamental to capturing the semantics of VLSI CAD design Proceedings of the Second International 15, 1988

**Ppt coordinator prof' a' k' majumdar powerpoint**

Coordinator Prof' A' K' Majumdar. Proceedings of 3rd IEEE International Workshop on Electronic Design, Conference on VLSI Design, January 2007, Bangalore, India.

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Bangalore, India, December 2009. Second International Research Workshop on Advances and Innovations in System Testing Proceedings of VLSI Design 2007,

**Curriculum vitae - klipsch school of electrical**

pp. 6.31 6.34, 1988. 18. IEEE Second International Workshop on Design of Mixed Mode Integrated Circuits and Proceedings of the VLSI03

**Spie | proceeding | vlsi design of low-leakage**

S. Solanki; F. Frustaci and P. Corsonello "VLSI design of low-leakage single-ended 6T SRAM cell", Proc. SPIE 8067, VLSI Circuits and Systems V, 80670R (May 03, 2011

**Publications - birla institute of technology and**

IEEE Proceeding of Second International Conference on Emerging Trends in Engineering India . 18. Subhendu Kumar Of 8th VLSI Design and Test Workshop, pp

### **Conferences - sanjayranka - google sites**

International Conference on VLSI Design Machine, Proceedings of First International Workshop on Parallel Processing, Bangalore, India, December

### **Proceedings of the 9th international conference**

Title: VLSID '96 Proceedings of the 9th International Conference on VLSI Design: VLSI in Mobile Communication table of contents

### **Resume of - kfupm**

Bangalore University, India, for Embedded Systems'. 9th International Workshop on . Placement in VLSI Circuit Design", In proceedings of 10th IEEE

### **(fcgs - springer**

1988 Workshop on VLSI Signal Pro- December 15-18 Bangalore, India Second International Workshop of VLSI Design (Computer Society of India)

### **Ramesh karri - publications | nyu polytechnic**

Proceedings of IEEE International Workshop on High Level Proceedings of the Second International Workshop on VLSI design, December 1988, Bangalore, India.

### **Vlsi design & test: milestones and challenges :**

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Habib and A. Parker, Proceedings of the 15 th International of VLSI Design Bangalore, India, December 1988. Proceedings of the Second Annual

### **Madras institute of technology**

Five days training program on Computer Networking for CVRDE Scientist in December 2002. Two days workshop on VLSI Design during March 28th and 29th 2005.

### **Digital integrated circuits: a design perspective**

IEEE Transactions on Very Large Scale Integration (VLSI) December 18-21, 2006, Bangalore, India Proceedings of the Second International Workshop on

### **Vlsi design and test - 17th international**

This book constitutes the refereed proceedings of the 17th International Symposium on VLSI Design and Test, VDAT 2013, held in Jaipur, India, in July

### **Jaime ramirez-angulo | klipsch school of**

IEEE Second International Workshop on Design of Mixed Mode Integrated Jaime Ramirez-Angulo Proceedings of the Third IEEE Bangalore India